

ISSUE CLASSIFICATION	
Class	Subclass

**PATENT NUMBER**

**U.S. UTILITY Patent Application**

<b>RW</b> O.I.P.E. SCANNED <b>Am</b> <b>GTH</b>	<b>PATENT DATE</b>
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APPLICATION NO. 09/717495	CONT/PRIOR	CLASS T12	SUBCLASS <del>T12</del>	ART UNIT 2115 <del>2189</del>	EXAMINER <del>Craige E.</del> Chandrasekhar
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Michael Fischer      713      500      —      ~~Chandrasekhar~~

TITLE      Sequencer and method of selectively inhibiting clock signals to execute reduced instruction sequences in a re-programmable I/O interface

PTO-2040  
12/99

[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____	_____ (Primary Examiner)		<b>ISSUE FEE</b>	
	_____ (Date)		Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)		<b>ISSUE BATCH NUMBER</b>	
_____ (Date)				

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